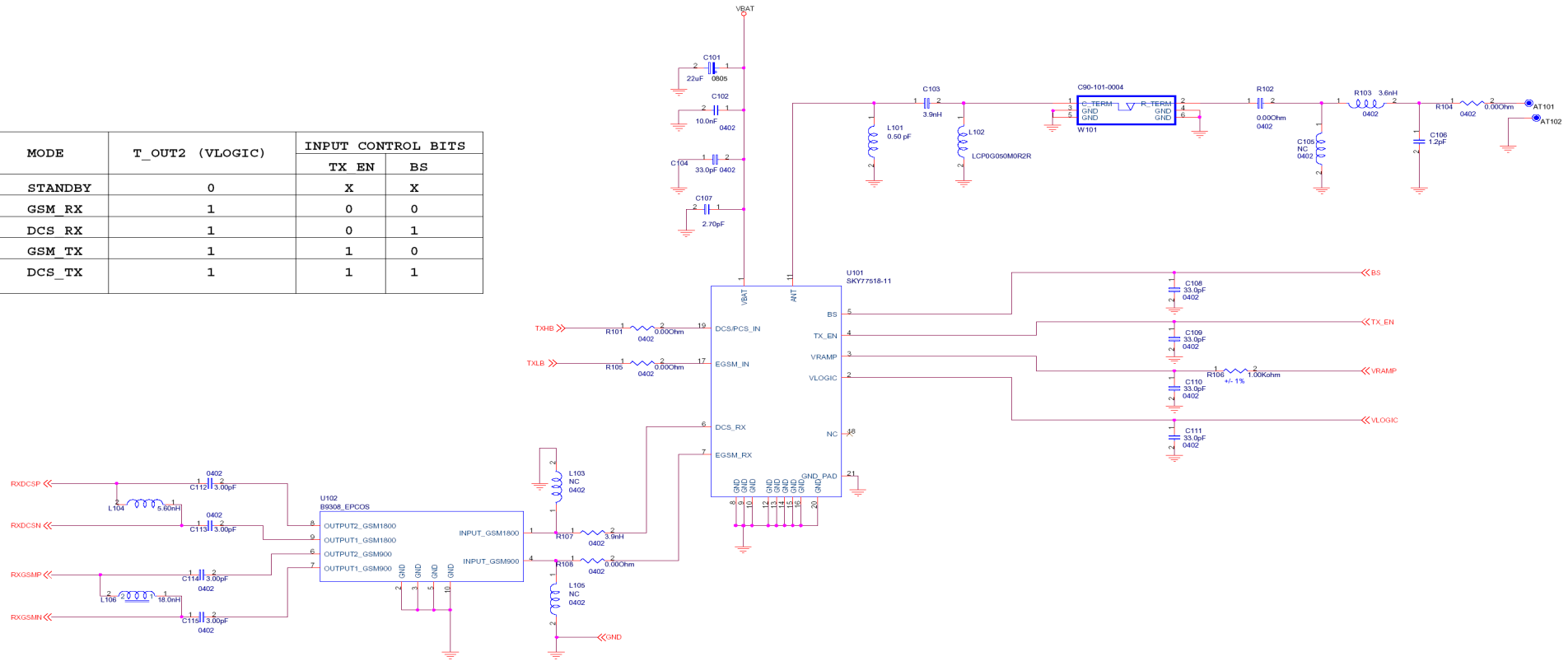


# 7. CIRCUIT DIAGRAM

MODE	T_OUT2 (VLOGIC)	INPUT CONTROL BITS	
		TX_EN	BS
STANDBY	0	X	X
GSM_RX	1	0	0
DCS_RX	1	0	1
GSM_TX	1	1	0
DCS_TX	1	1	1



Parameter	Symbol	Test condition	Min	Type	Max	Unit
Supply voltage	VCC	-----	2.7	3.3	4.8	V
VLOGIC control voltage	LOW	VLOGIC_LOW	-0.1	---	0.5	V
	HIGH	VLOGIC_HIGH	1.2		VCC	
Supply current	ICC	-----	0		1.8	A
VLOGIC current	ILOGIC	VLOGIC ≤ 2.7 V TX_EN ≤ 0.4 V BS ≤ 0.4 V	---	1	20	uA

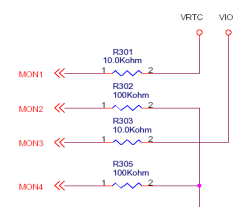
**MON pin configuration**

MON1 0 = LMEM@1.8V  
1 = LMEM@2.85V

MON2 0 = INT boot  
1 = EXT boot

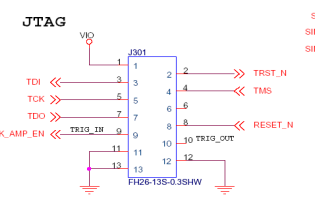
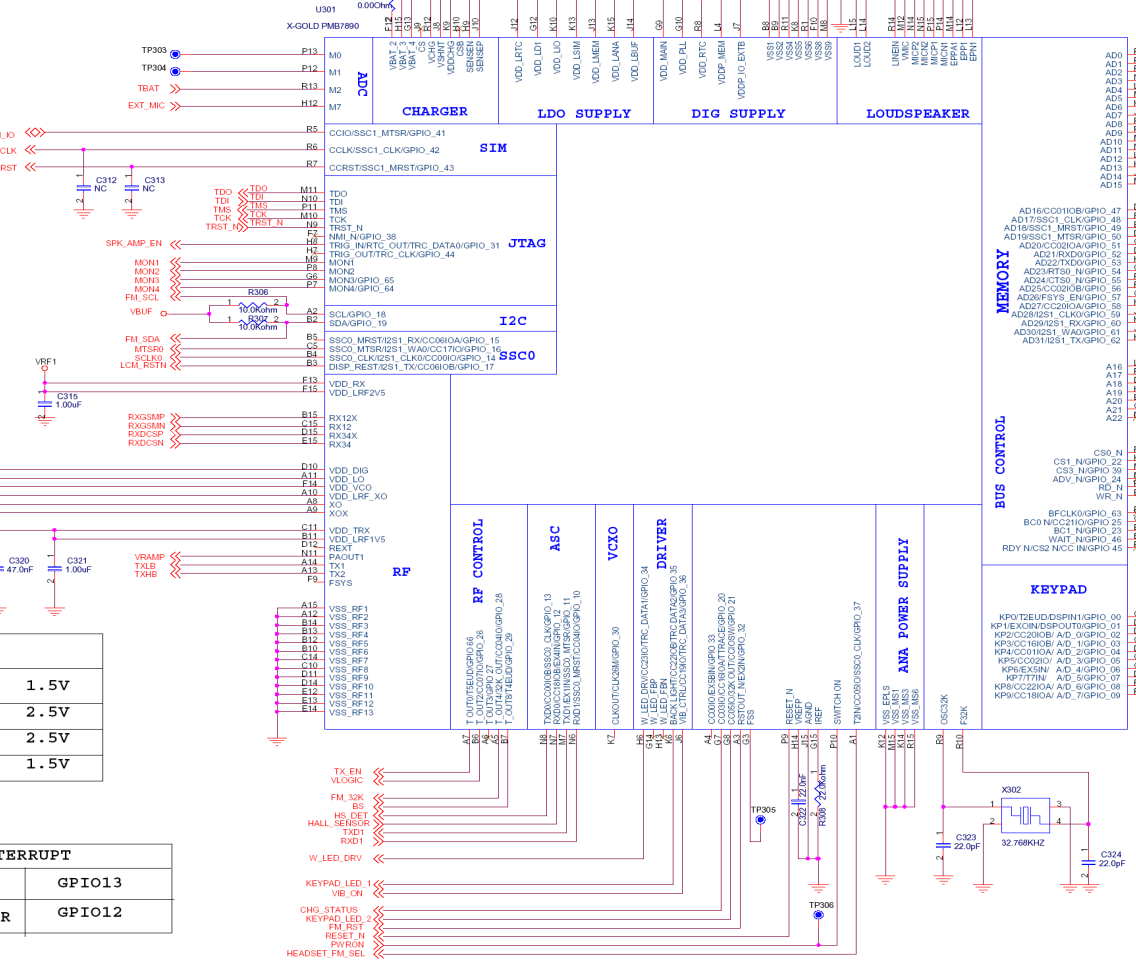
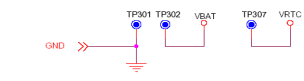
MON3 0 = MUX device  
1 = DEMUX device

MON4 0 = Parallel device  
1 = Serial device



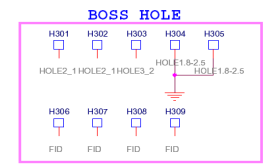
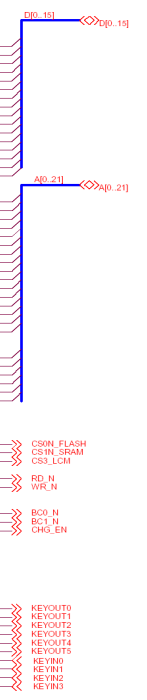
DIGITAL		
VDD_LD1	VLD1	1.5V
VDD_LRTC	VRTC	2.0V
VDD_LIO	VIO	2.85V
VDD_SIM	VSIM	1.8V/2.85V
VDD_LMEM	VMEM	2.85V

ANALOG		
VDD_LANA	VANA	2.5V
VDD_LBUF	VBUF	3.2V

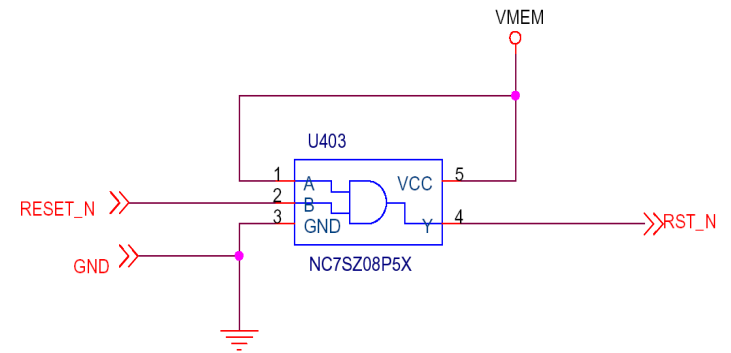
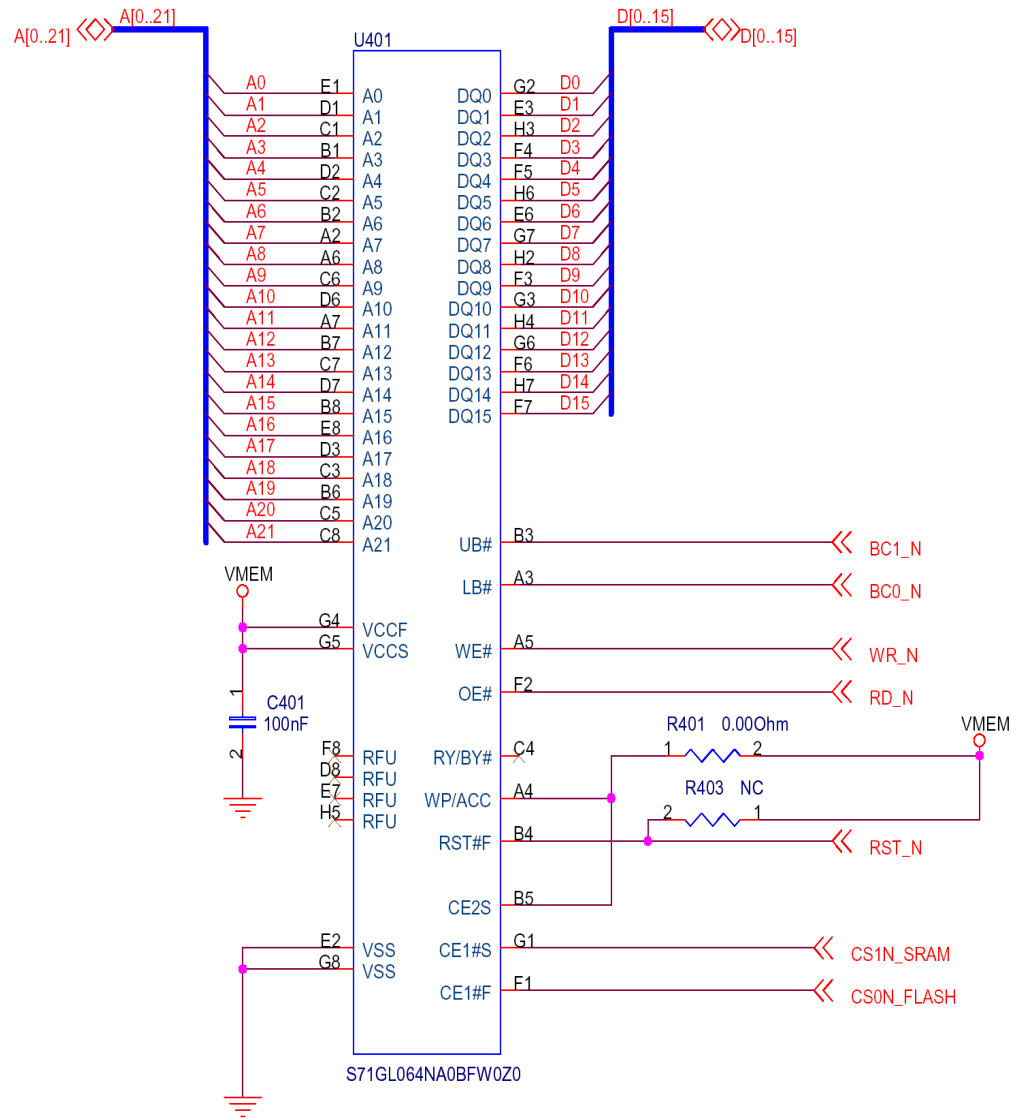


RF		
VDD_LRF1V5	VRFO	1.5V
VDD_LRF2V5	VRF1	2.5V
VDD_LRF_XO	VRF2	2.5V
VDD_DIG	VD1	1.5V

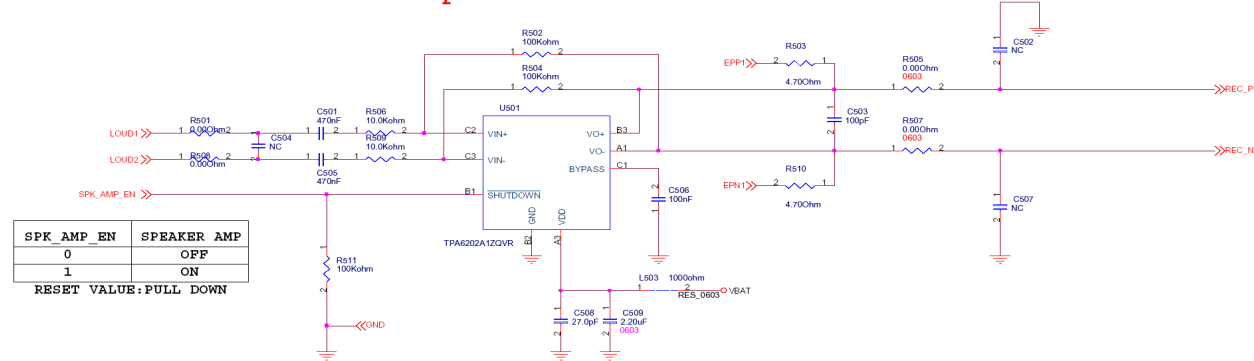
INTERRUPT	
HS_DET	GPIO13
HALL_SENSOR	GPIO12



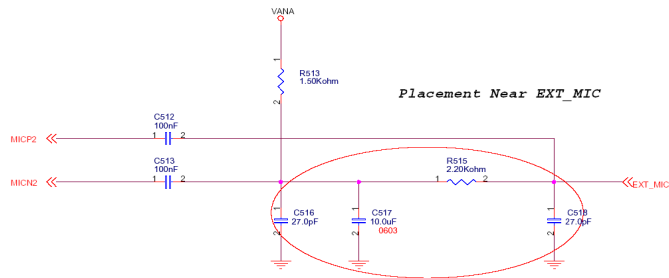
Arima Communications Corp.			
LG Sapphire-S			
File	Document Number	Baseband PMB7890	Rev
Size	C		1/0



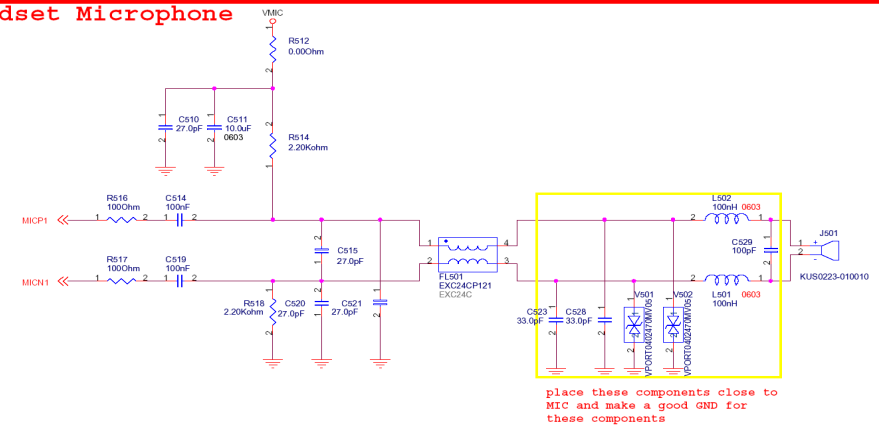
### Speaker&Receiver



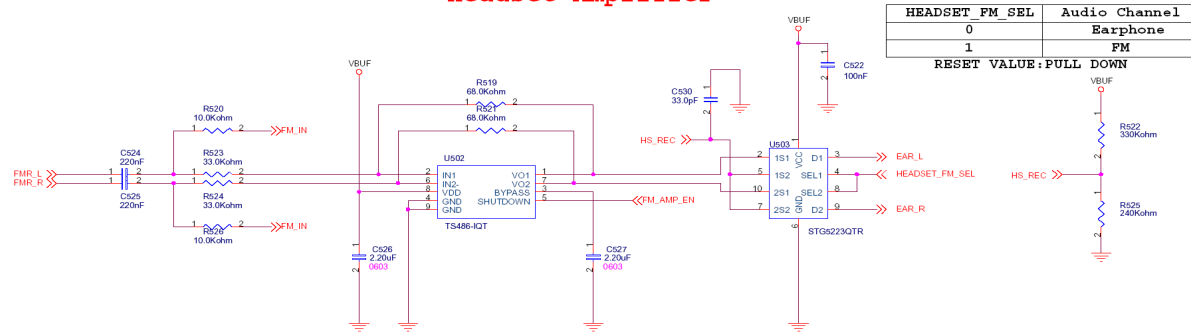
### Headset Micophone



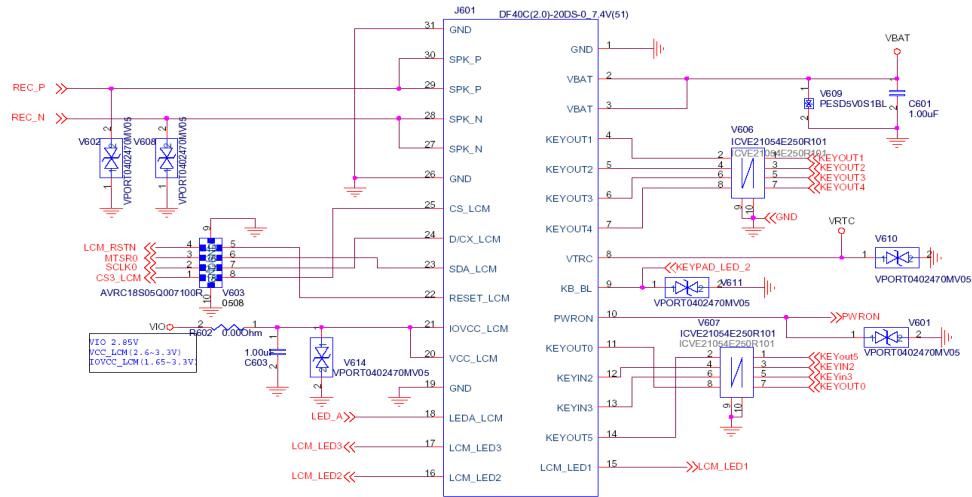
### Handset Microphone



### Headset Amplifier



### CONNECTOR

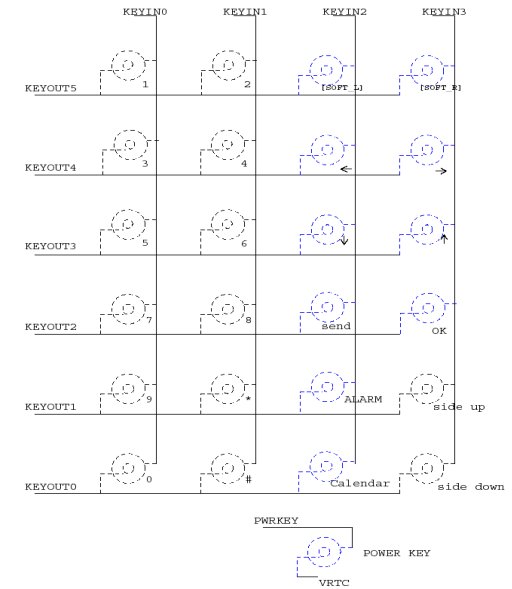


place all these parts close to I/O connector

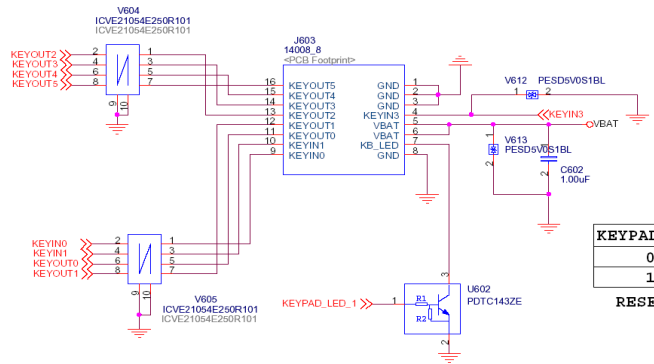
KEYPAD_LED_2	KEYPAD2_LED
0	OFF
1	ON

RESET VALUE: PULL DOWN

### KEY Function

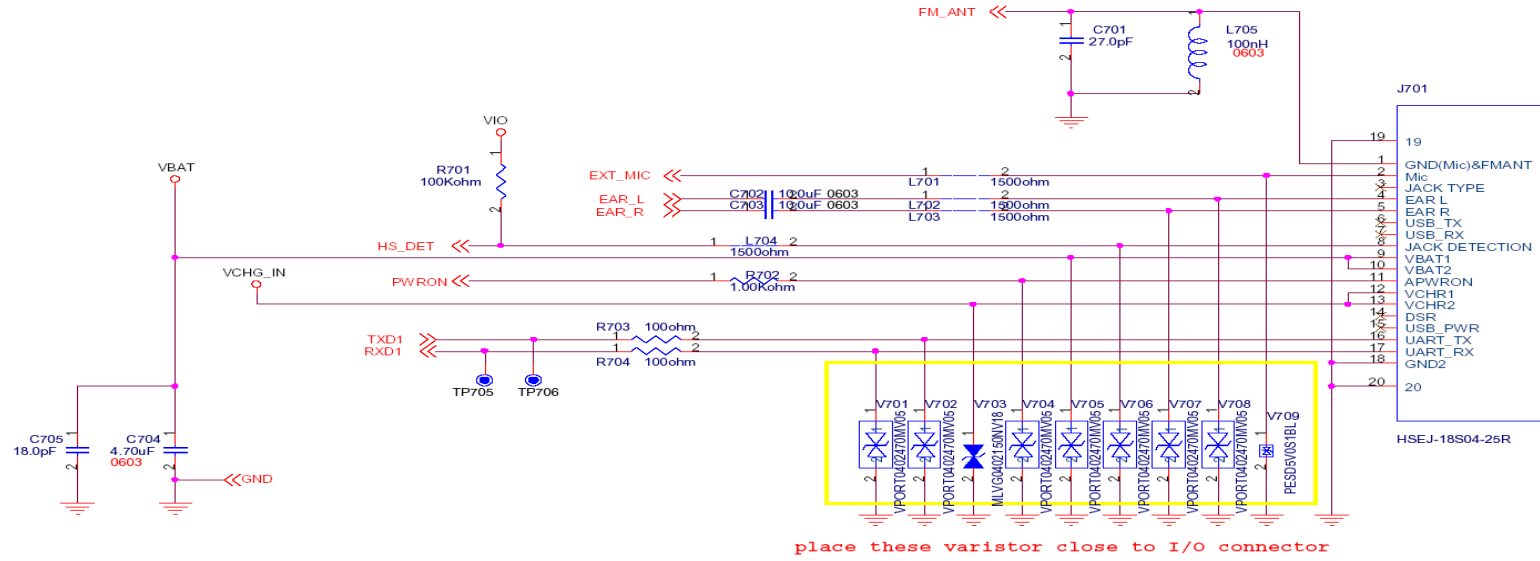


### KEY PAD connector

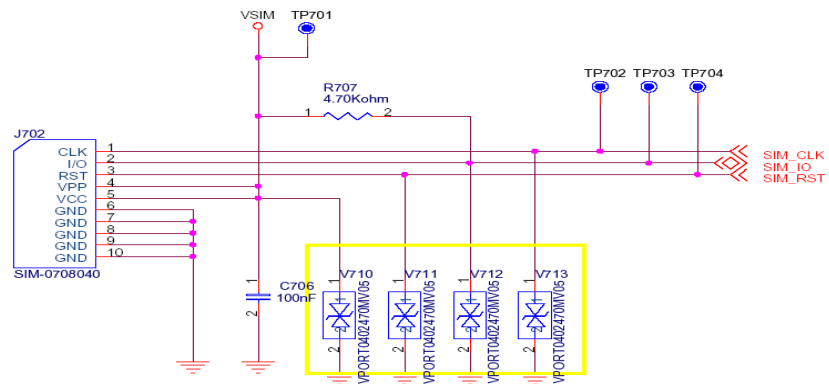


KEYPAD_LED_1	KEYPAD1_LED
0	OFF
1	ON

RESET VALUE: PULL DOWN



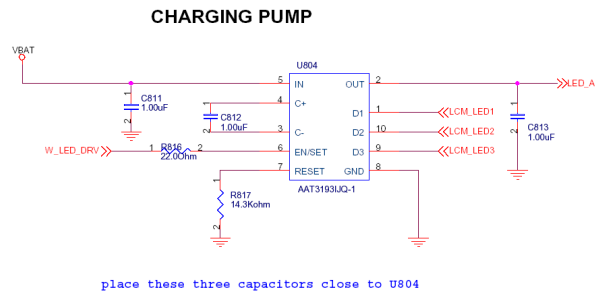
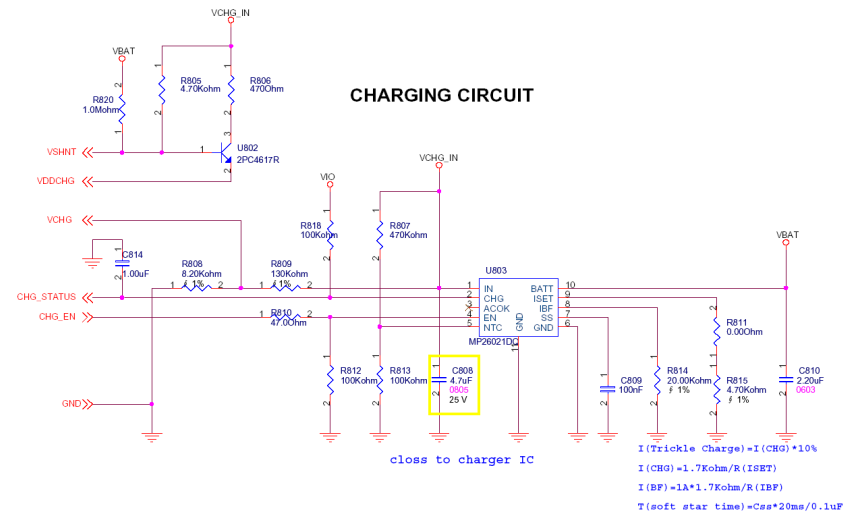
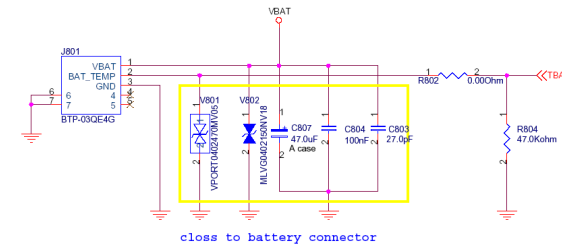
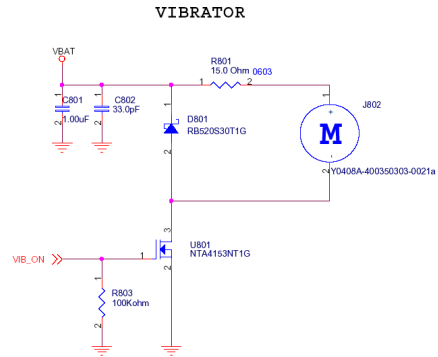
place these varistor close to I/O connector



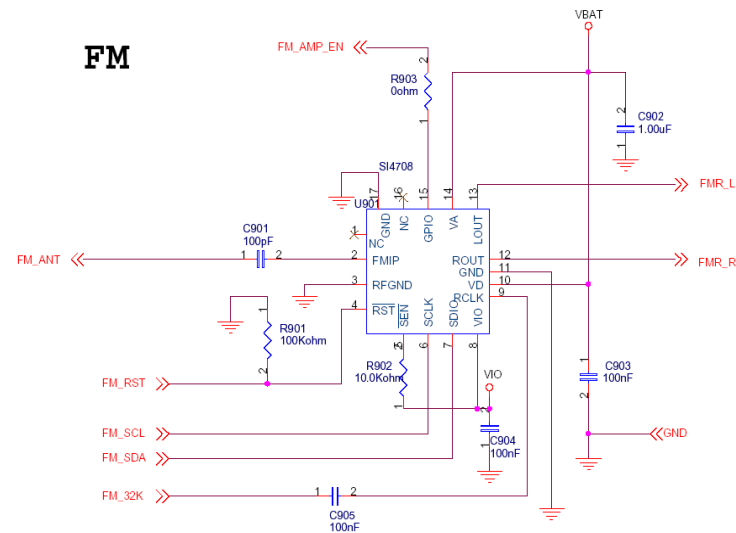
place these varistor close to SIM connector

VIB_ON	VIB state
0	OFF
1	ON

RESET VALUE:PULL DOWN

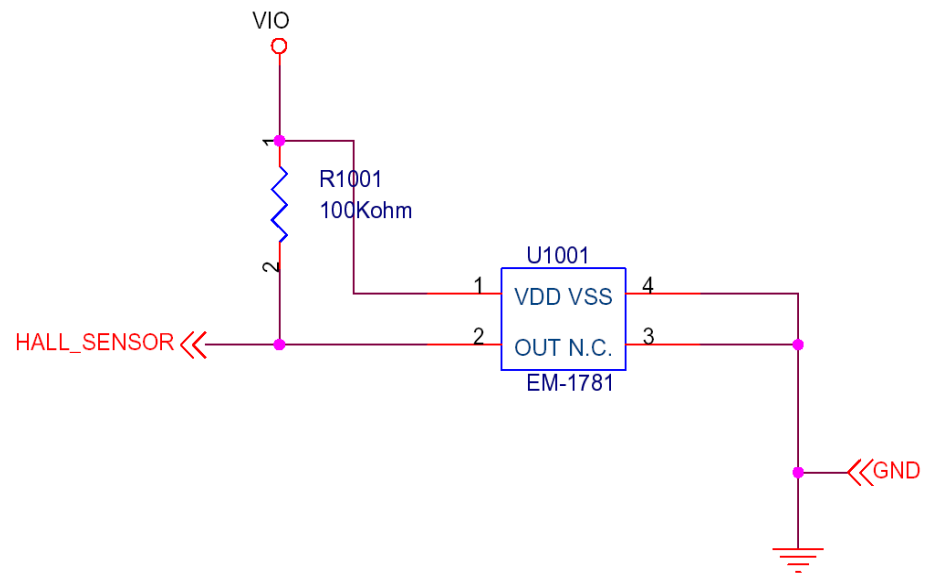


Parameter	Symbol	Test condition	Min	Type	Max	Unit
Digital supply voltage	VD		2.7	---	5.5	V
Analog supply voltage	VA		2.7	---	5.5	V
Analog powerdown current	IPDA	ENABLE=0	---	1.5	5	uA
Digital powerdown current	IPDD	ENABLE=0	---	1	6	uA





# HALL SENSOR



# 9. PCB LAYOUT

