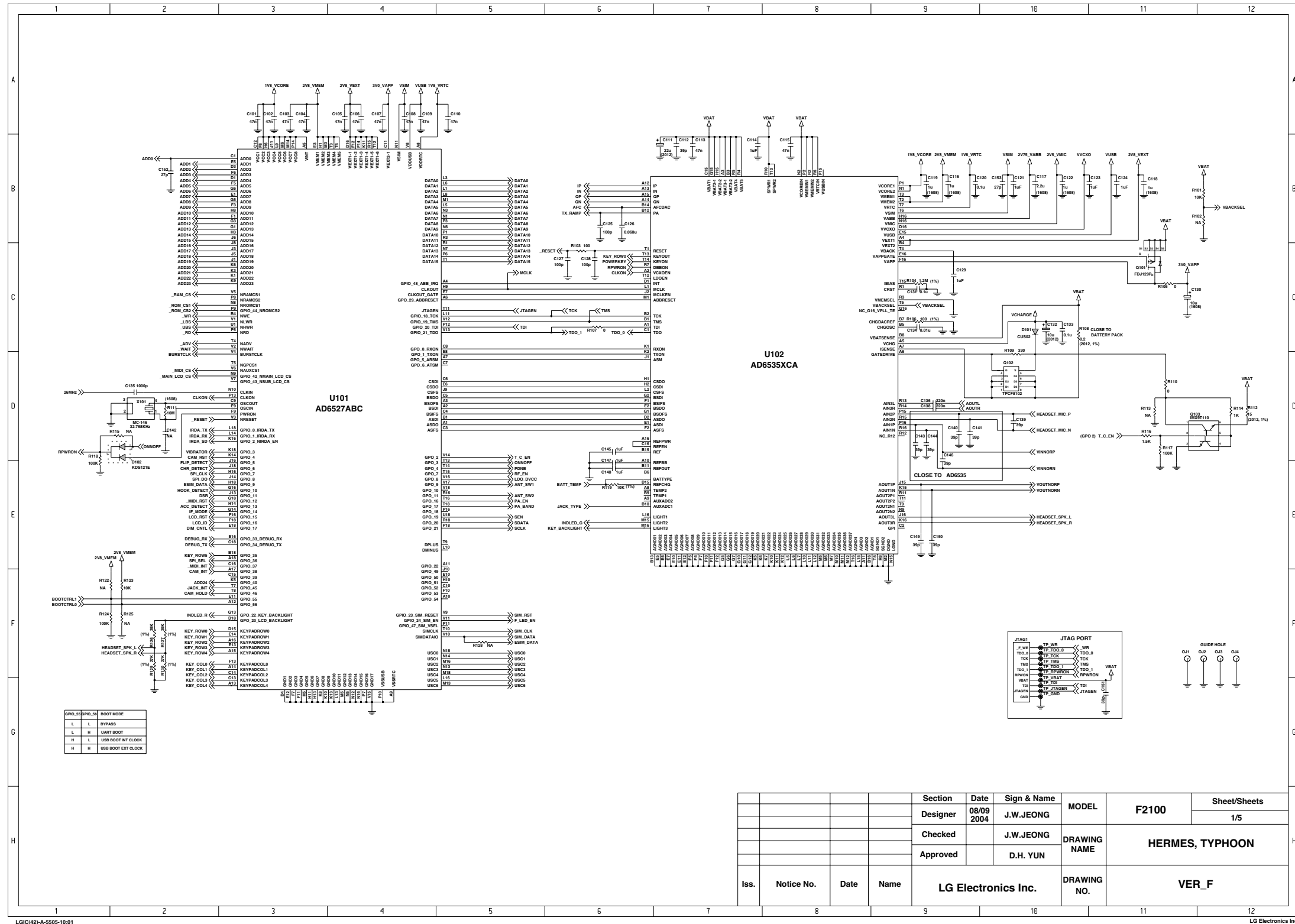
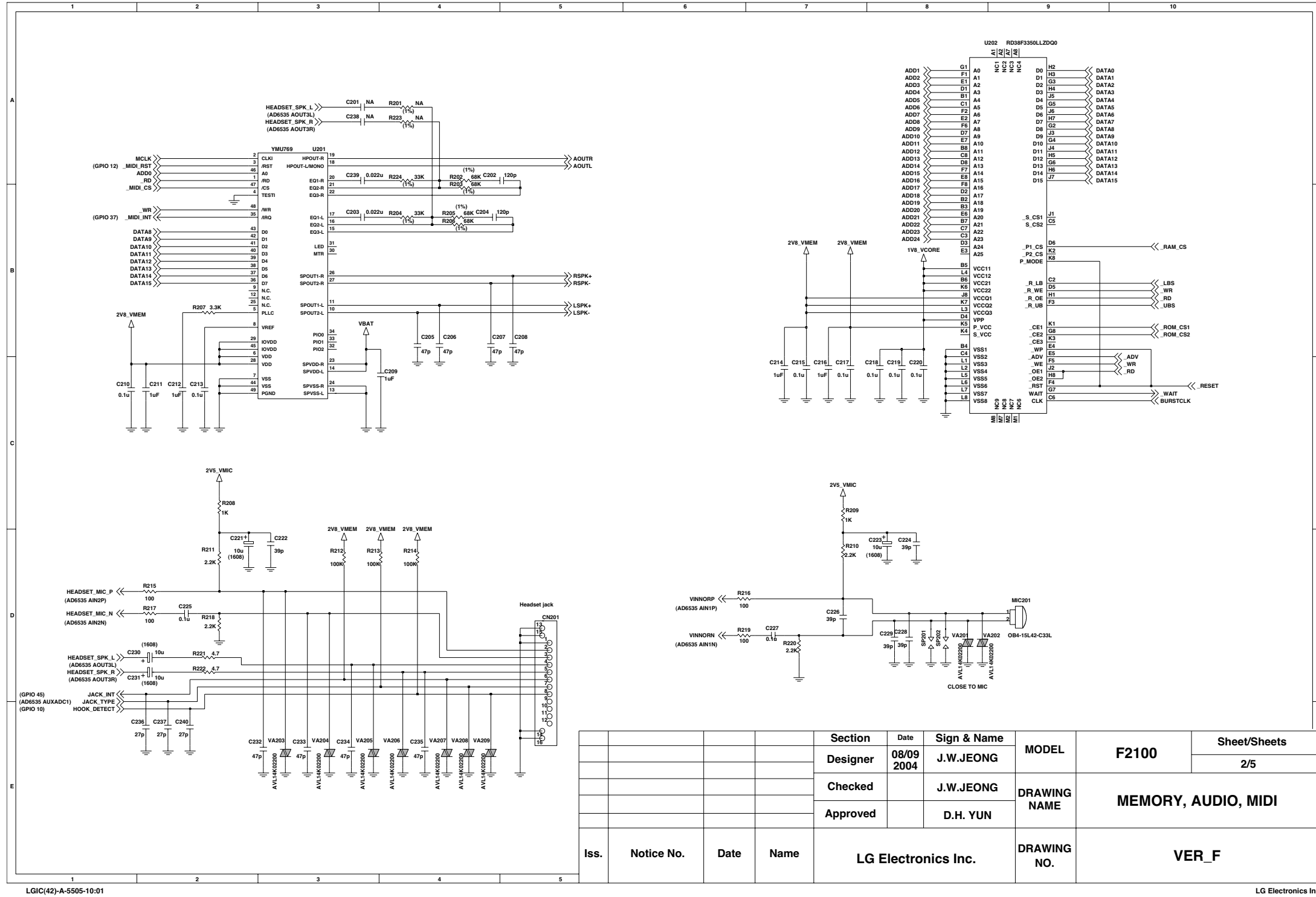


8. ПРИНЦИПИАЛЬНАЯ СХЕМА



Section	Date	Sign & Name	MODEL	F2100	Sheet/Sheets
Designer	08/09 2004	J.W.JEONG			1/5
Checked		J.W.JEONG			
Approved		D.H. YUN			
Iss.	Notice No.	Date	Name	LG Electronics Inc.	DRAWING NO.
					VER_F

8. ПРИНЦИПИАЛЬНАЯ СХЕМА

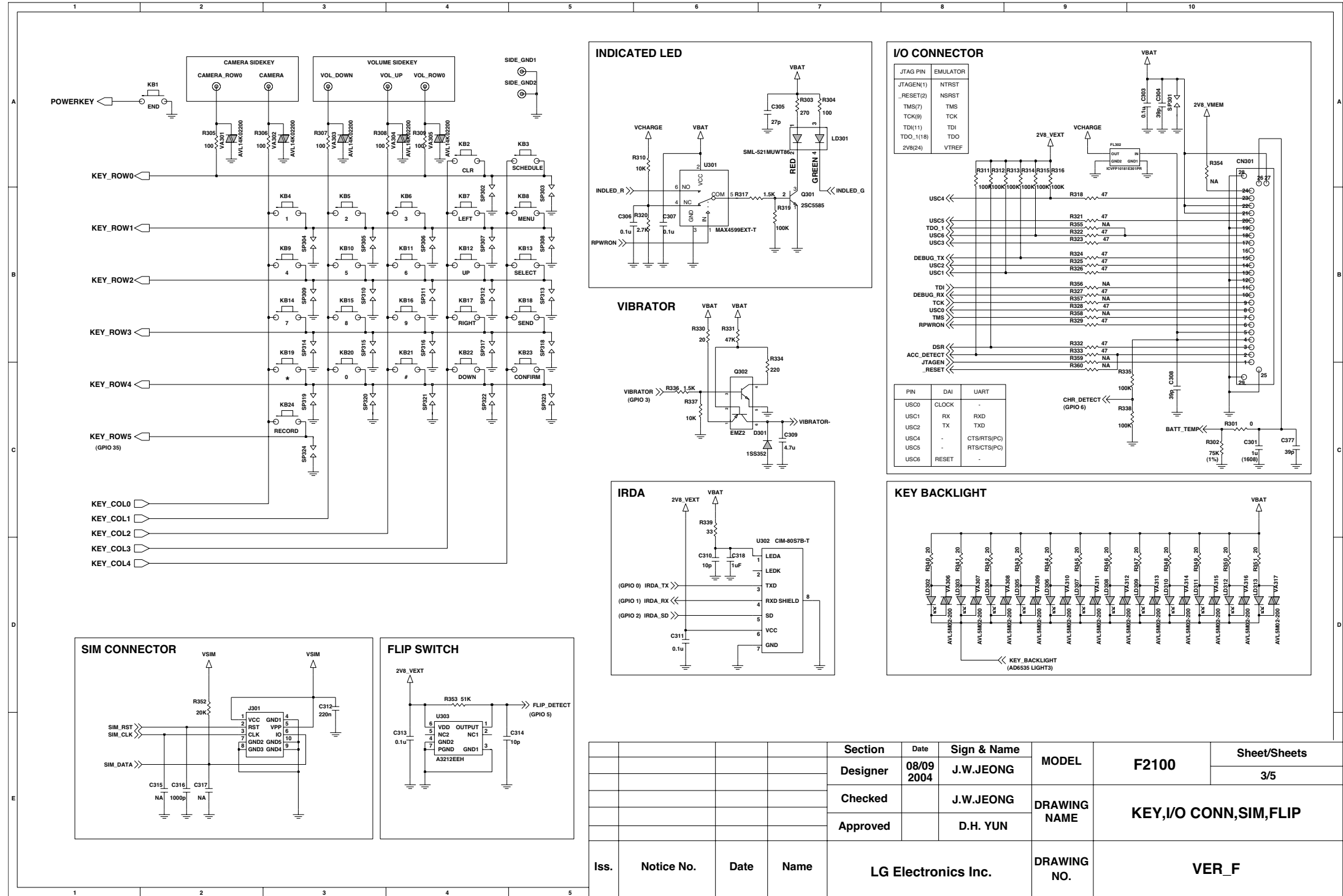


Section	Date	Sign & Name	MODEL	F2100	Sheet/Sheets
Designer	08/09 2004	J.W.JEONG			2/5
Checked		J.W.JEONG	DRAWING NAME	MEMORY, AUDIO, MIDI	
Approved		D.H. YUN			
Iss.	Notice No.	Date	Name	LG Electronics Inc.	DRAWING NO.
					VER_F

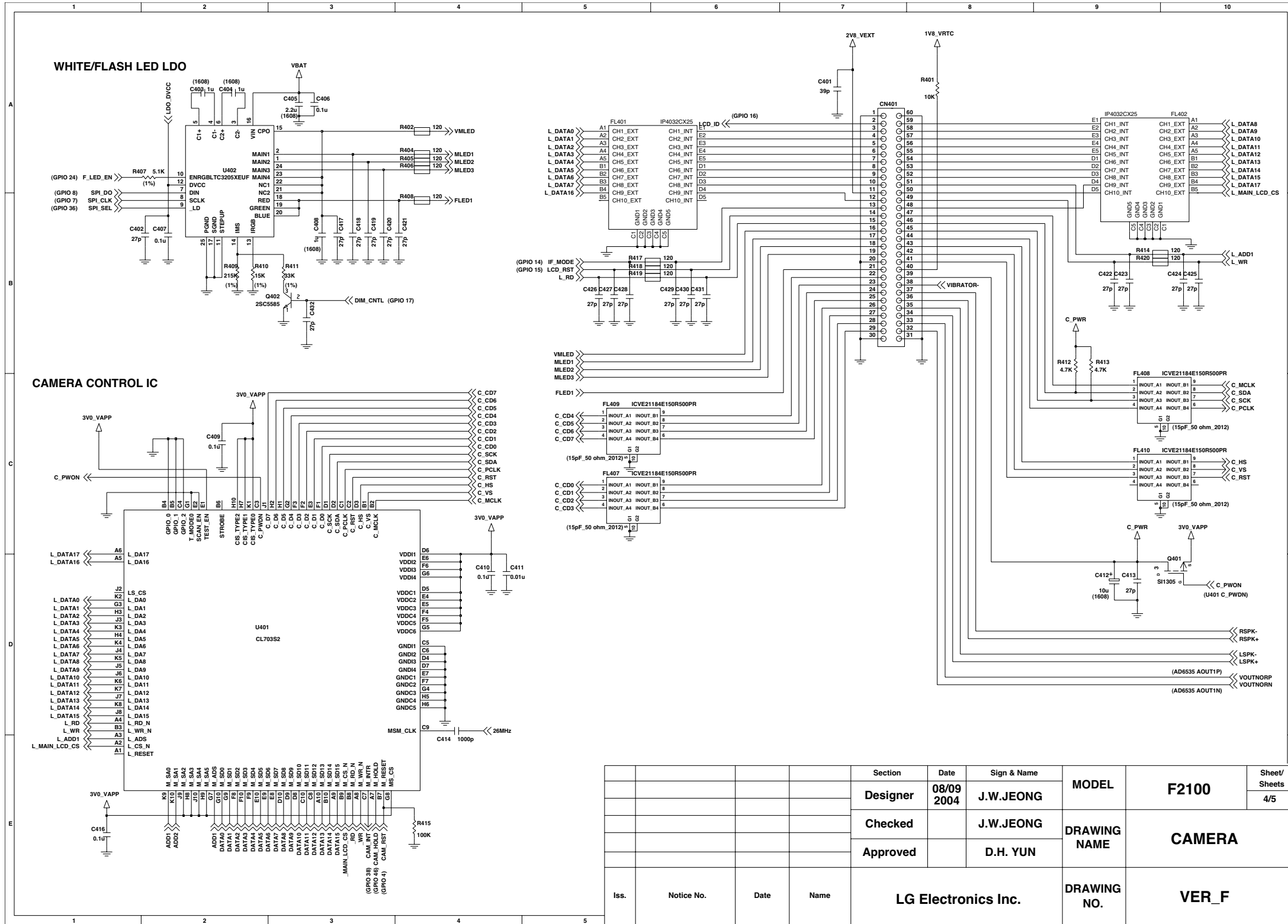
LGIC(42)-A-5505-10-01

LG Electronics Inc.

8. ПРИНЦИПИАЛЬНАЯ СХЕМА



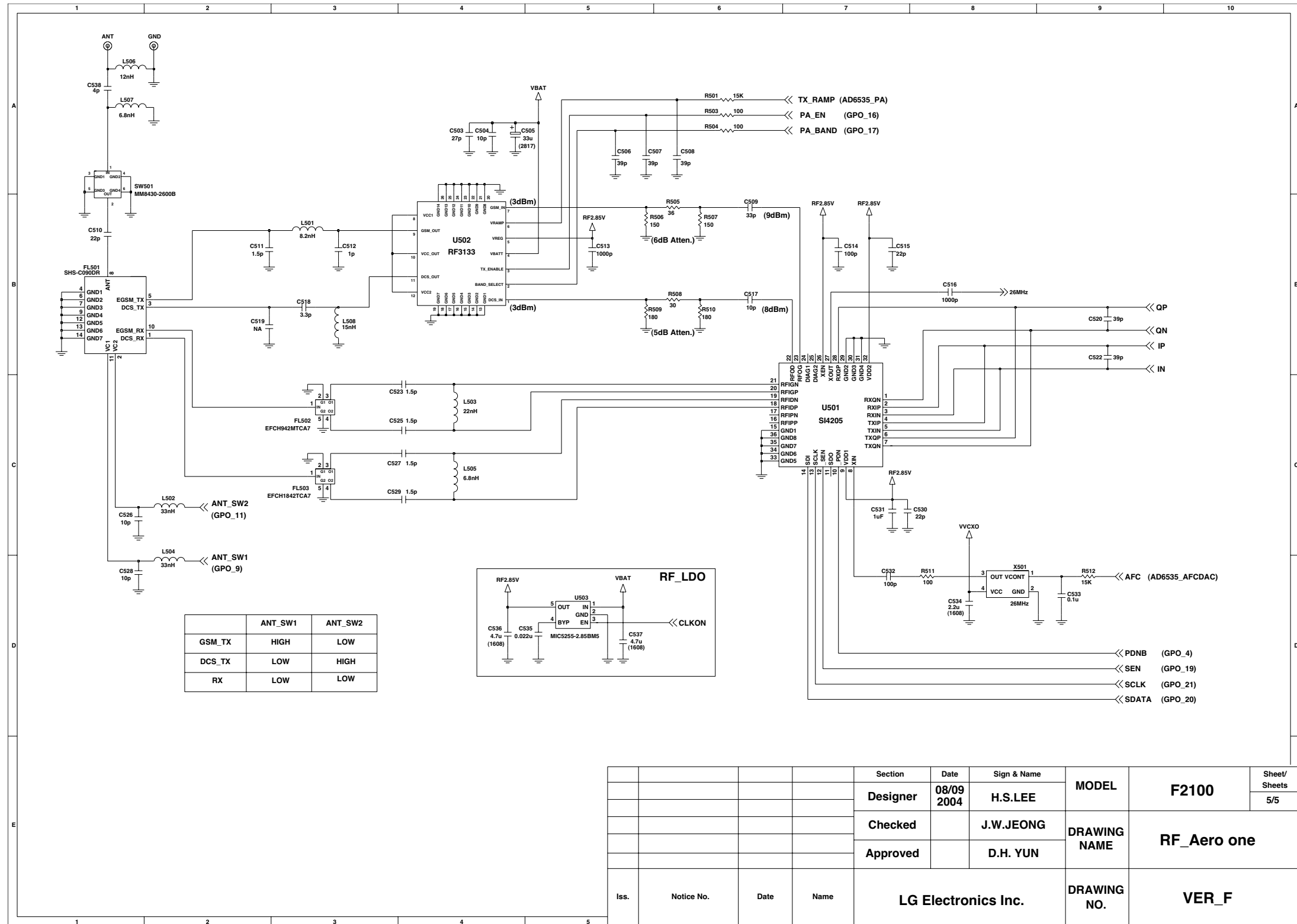
8. ПРИНЦИПИАЛЬНАЯ СХЕМА



LGIC(42)-A-5505-10:01

LG Electronics Inc.

8. ПРИНЦИПИАЛЬНАЯ СХЕМА



LGIC(42)-A-5505-10:01

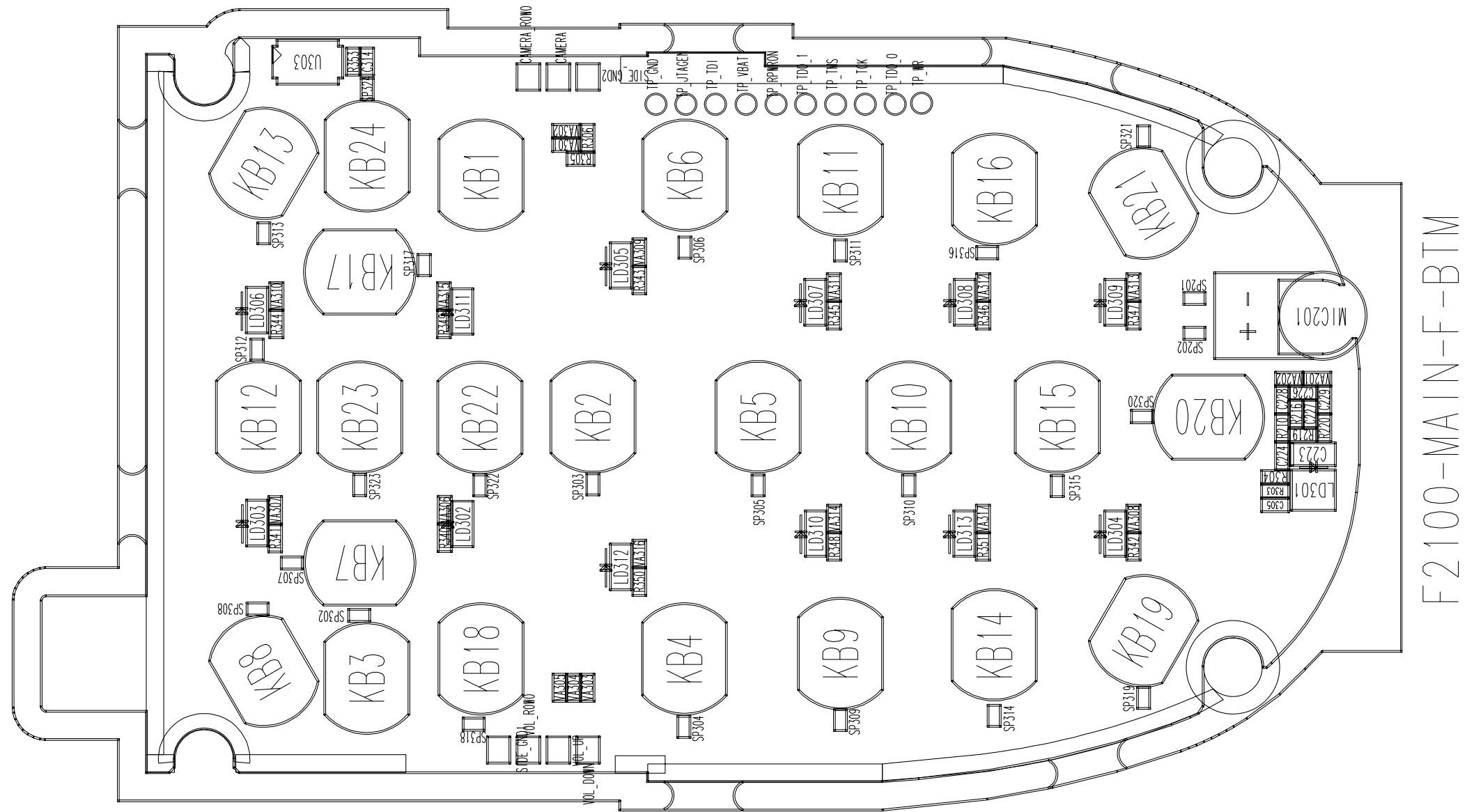
LG Electronics Inc.

Section	Date	Sign & Name	MODEL	F2100	Sheet/ Sheets
Designer	08/09 2004	H.S.LEE			5/5
Checked		J.W.JEONG	DRAWING NAME	RF_Aero one	
Approved		D.H. YUN			
Iss.	Notice No.	Date	Name	LG Electronics Inc.	DRAWING NO.
					VER_F

9. РАСПОЛОЖЕНИЕ ЭЛЕМЕНТОВ НА ПЕЧАТНОЙ ПЛАТЕ



9. РАСПОЛОЖЕНИЕ ЭЛЕМЕНТОВ НА ПЕЧАТНОЙ ПЛАТЕ



F2100-MAIN-F-BTM